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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Filing Date January 20, 1999 Assignee Micron Technology, Inc. Examiner D. Vu Attorney Docket No. Ml22-1035 Title: Semiconductor Processing Methods

Commissioner for Patents PO Box 1450 Art Unit 2818 Alexandria, VA 22313-1450

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- Transmittal form PTO/SB/21 2.
- Information Disclosure Statement; PTO-1449, cited reference

Vet. 15,2004

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Under the Paperwork Reduction 744 of 1935, IN	Application Number	09/234,233								
TRANSMITTAL	Filing Date	January 20, 1999								
FORM	First Named Inventor	Weimin Li								
(to be used for all correspondence after initial filing	g) Art Unit	2818								
	Examiner Name	D. Vu								
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1035								
ENCLOSURES (Check all that apply)										
Certified Copy of Priority Document(s) Cu		After Allowance Communication to a Technology Center (TC) Appeal Communication to Board of Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief, Repty Brief) Proprietary Information Status Letter Other Enclosure(s) (please Identify below): PTO-1449 (1) reference								
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SIGNATU	RE OF APPLICANT, ATTORNI	EY, OR AGENT								
Firm James E. Lake or Reg. No. 44,854, Wells St. Jo lndlvidual Signature	ohn P.S.									
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form end/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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OCT 1 5 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No
Filing Date January 20, 1999
Inventor
Micron Technology Inc.
Assignee Micron Technology, Inc.
Group Art Unit
Examiner
Attorney's Docket No
Title: Semiconductor Processing Methods

INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of a Request for Continued Examination (RCE) Application and prior to the receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of this reference is respectfully requested.

Respectfully submitted,

Dated: 15 Oct 2004

James E. Lake

Reg. No. 44,854

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		-		U.S. PATENT DOCUMENTS					
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